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Approved  
TP  
4/30/03

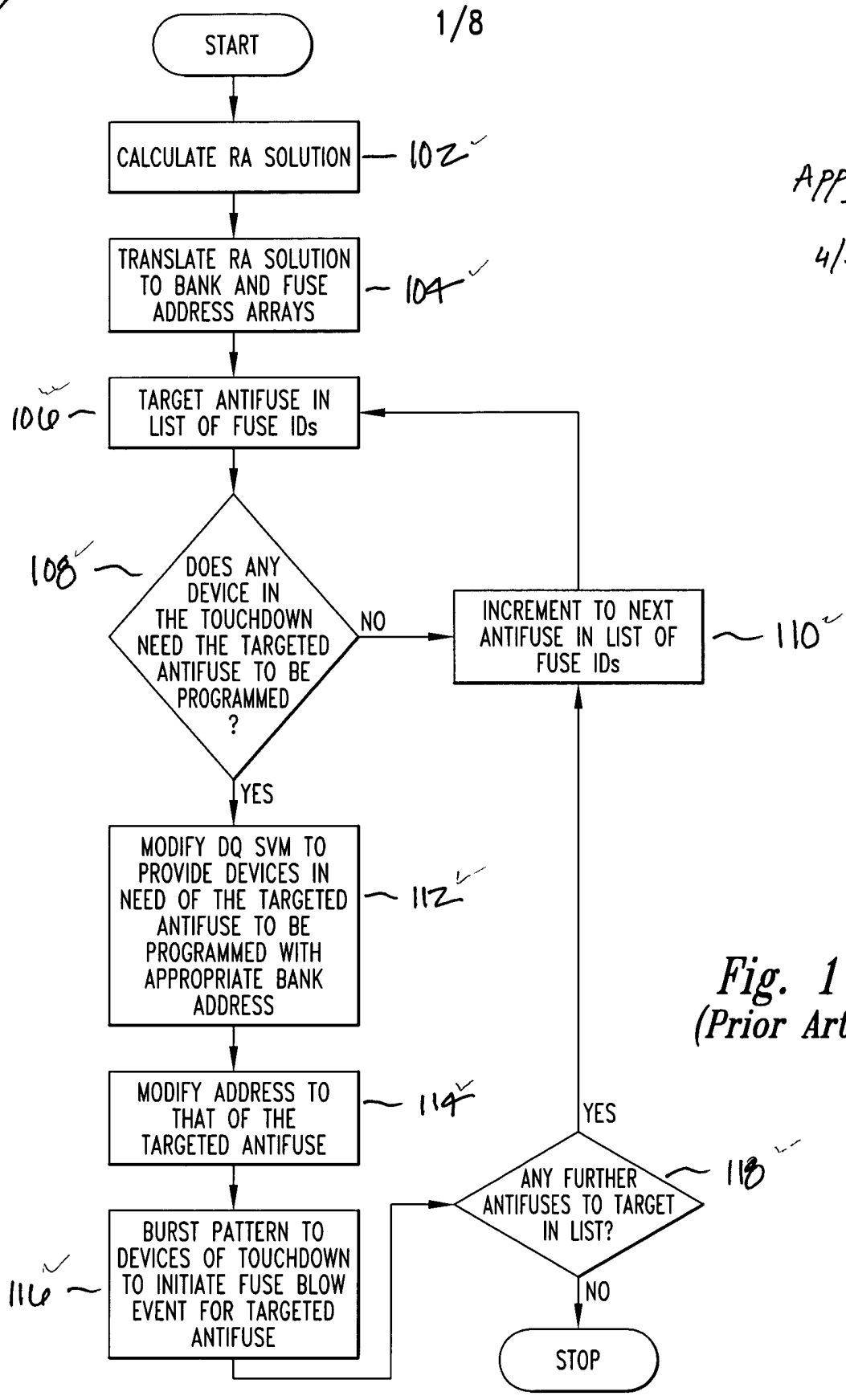


Fig. 1  
(Prior Art)

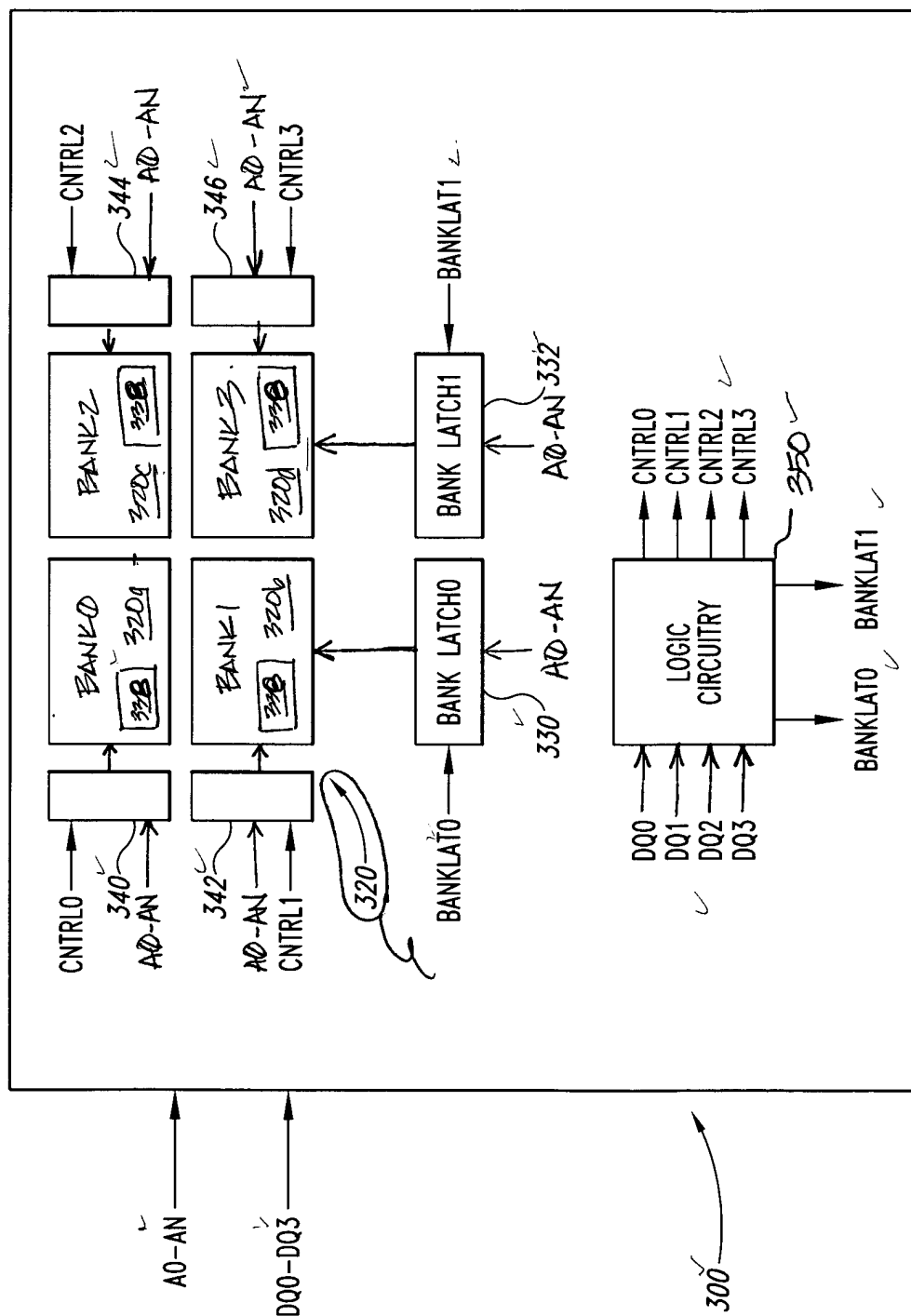


Fig. 2

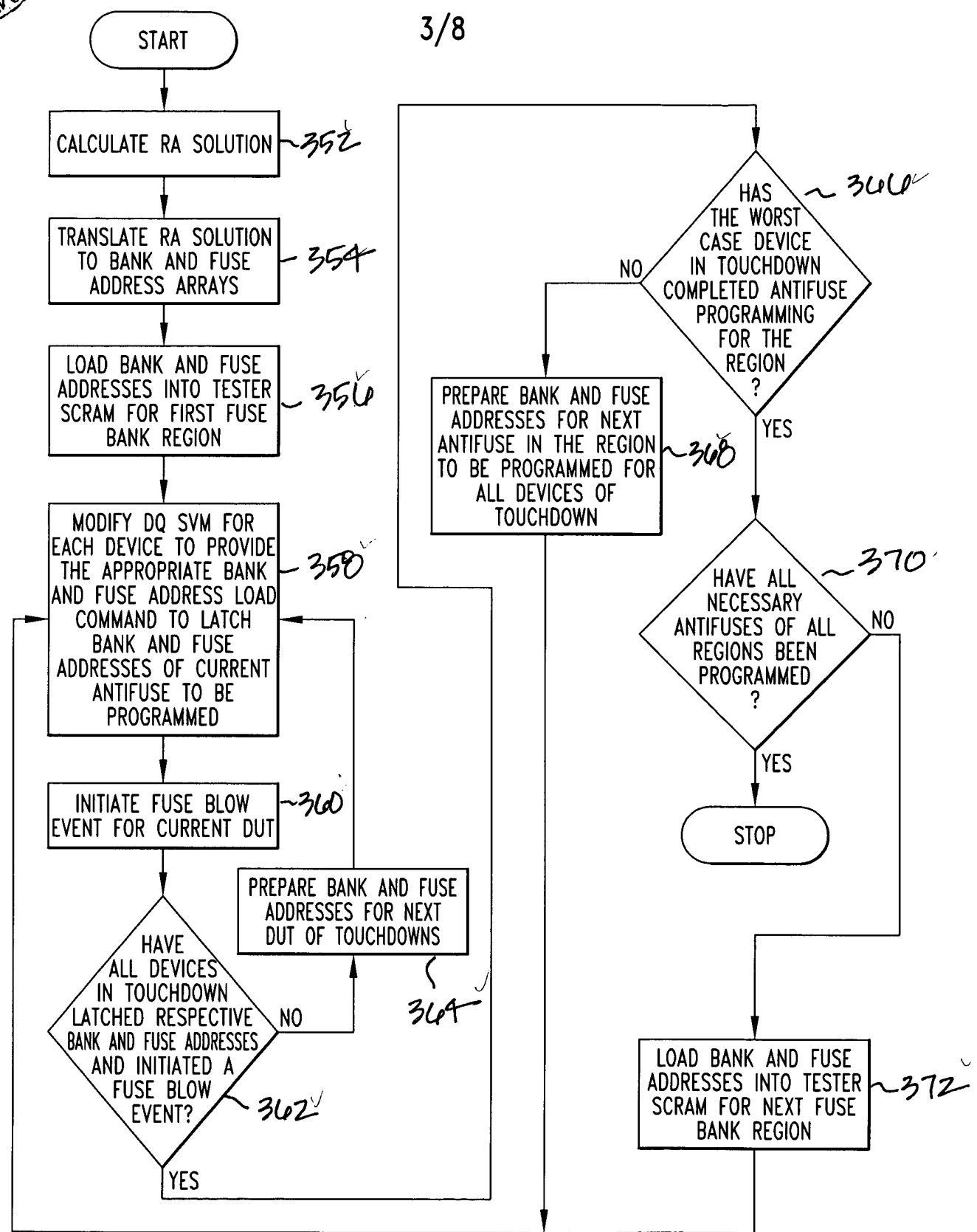
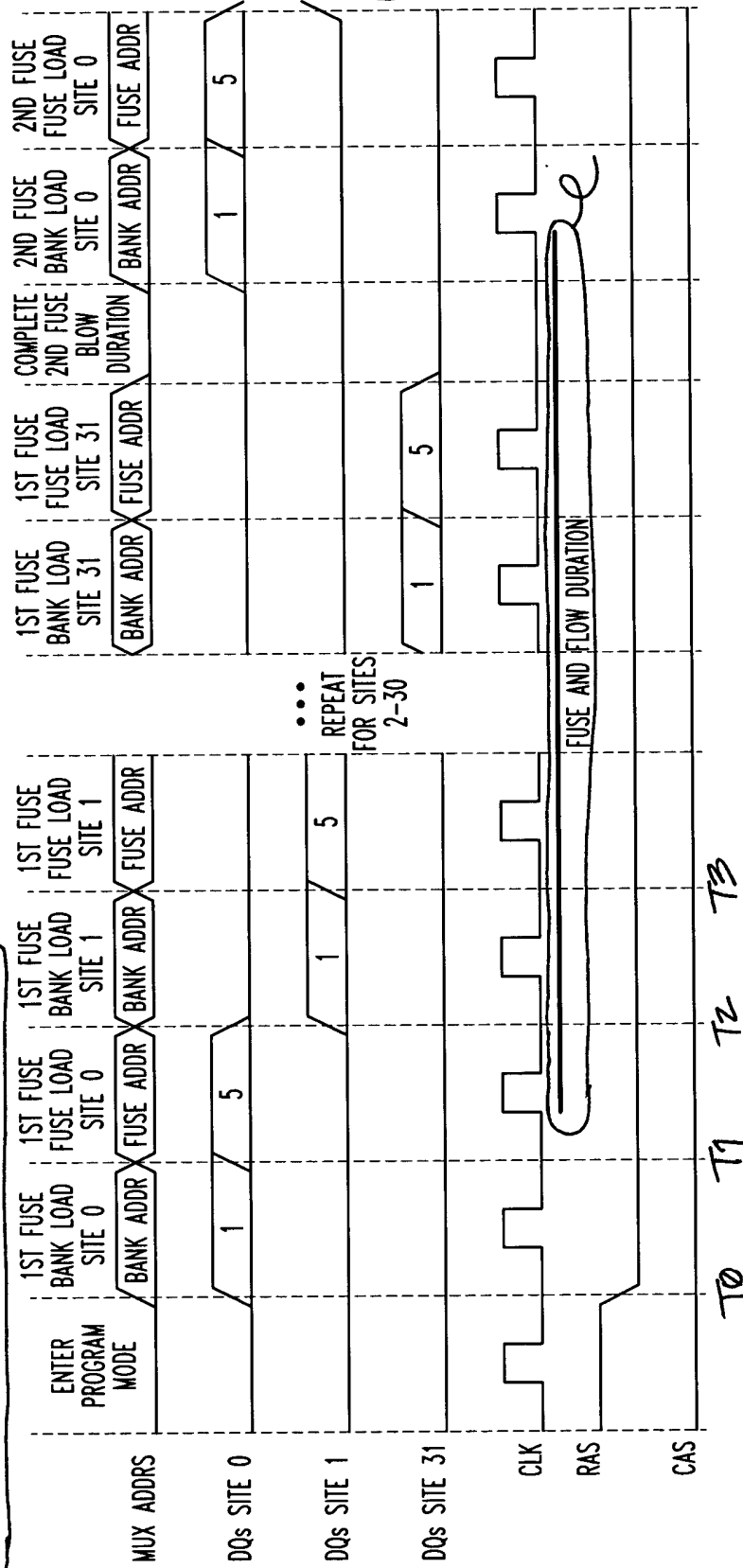


Fig. 3

**SIMPLIFIED WAVEFORM FOR J994 FUSE BLOW METHOD**

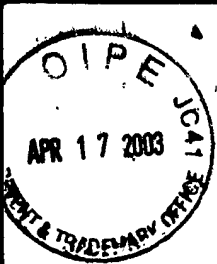


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**DQs FOR BANK AND FUSE ADDRESS LOADING**

3210	HEX	FUNCTION
0000	0	LOAD NOTHING
0001	1	LOAD BANK ADDRESS LEFT
0010	2	LOAD BANK ADDRESS RIGHT
0101	5	LOAD FUSE ADDRESS TOP LEFT
0110	6	LOAD FUSE ADDRESS TOP RIGHT
1001	9	LOAD FUSE ADDRESS BOTTOM LEFT
1010	A	LOAD FUSE ADDRESS BOTTOM RIGHT

**Fig. 4**



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500

SCRAM RAM		DQ SVM DATA							
		DUT 0	DUT 1	DUT 0	DUT 1	DUT 31	DUT 31	DUT 63	DUT 63
		0123	0123	0123	0123	0123	0123	0123	0123
502	BANK	1000	0000	...	...	0000	...	0000	...
504	FUSE	1010	0000	...	...	0000	...	0000	...
502	BANK	0000	1000	...	...	0000	...	0000	...
504	FUSE	0000	1010	...	...	0000	...	0000	...
...									
502	BANK	0000	0000	...	...	1000	...	0000	...
504	FUSE	0000	0000	...	...	1010	...	0000	...
...									
502	BANK	0000	0000	...	...	0000	...	1000	...
504	FUSE	0000	0000	...	...	0000	...	1010	...

Fig. 5



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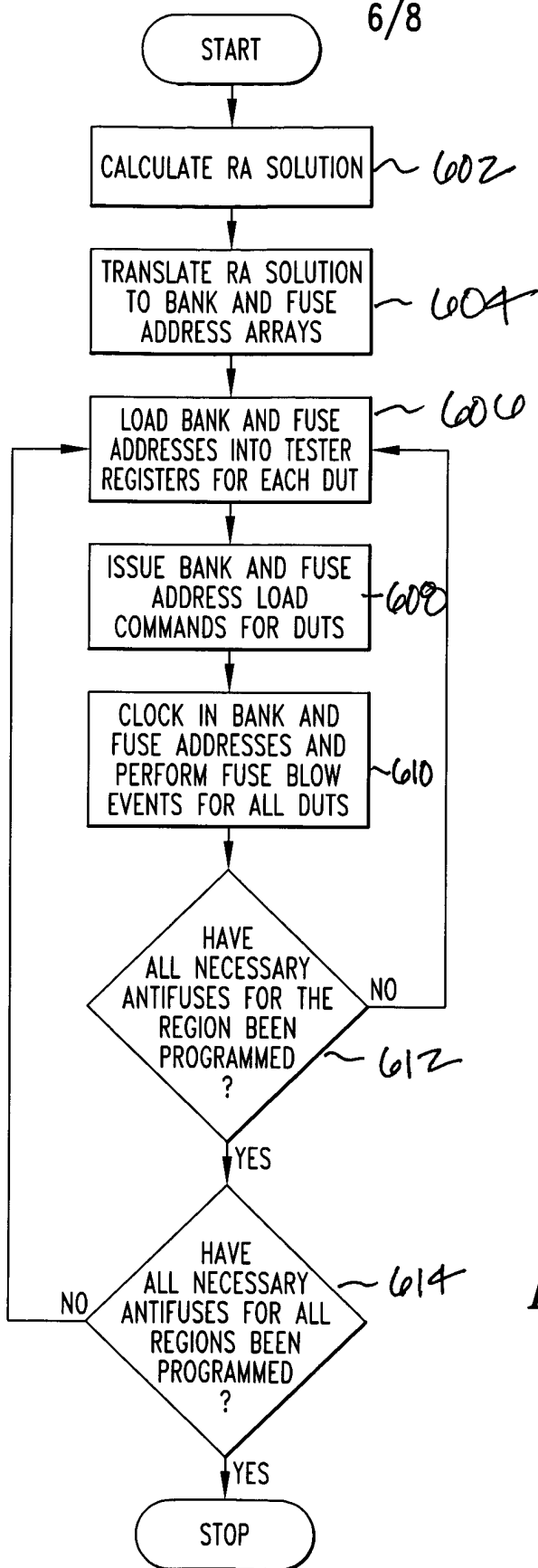
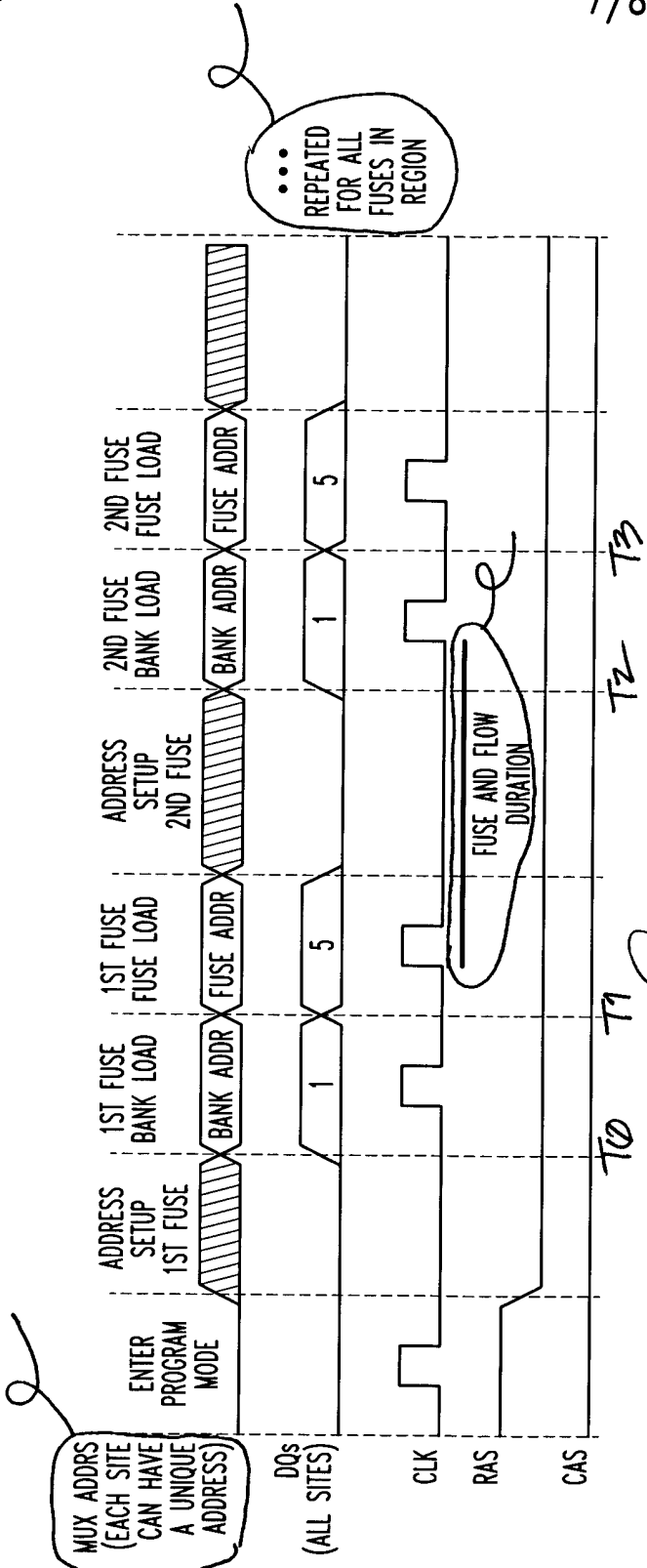


Fig. 6



DQs FOR BANK AND FUSE ADDRESS LOADING

HEX	FUNCTION
3210	LOAD NOTHING
0000	LOAD BANK ADDRESS LEFT
0001	LOAD BANK ADDRESS RIGHT
0010	LOAD FUSE ADDRESS TOP LEFT
0101	LOAD FUSE ADDRESS TOP RIGHT
0110	LOAD FUSE ADDRESS BOTTOM LEFT
1001	LOAD FUSE ADDRESS BOTTOM RIGHT
1010	LOAD FUSE ADDRESS BOTTOM RIGHT

DEVICE LAYOUT

DEVICE BANK (FUSE REGION) 0		DEVICE BANK (FUSE REGION) 2	
DQs	3210	DQs	3210
0001	LOAD BANK ADDRESS	0010	LOAD BANK ADDRESS
0101	LOAD FUSE ADDRESS	0110	LOAD FUSE ADDRESS

DEVICE BANK (FUSE REGION) 1		DEVICE BANK (FUSE REGION) 3	
DQs	3210	DQs	3210
0001	LOAD BANK ADDRESS	0010	LOAD BANK ADDRESS
1001	LOAD FUSE ADDRESS	1010	LOAD FUSE ADDRESS

Fig. 7